EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"10584592".rlan. or ("10". src. and "584592".ap.)	US-PGPUB; USPAT; USOCR; DERWENT	OR	OFF	2011/01/13 11:20
S2	1	Method NEAR3 Camouflaging NEAR3 Functions NEAR3 Electronic NEAR3 Circuit	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2011/01/13 11:26
S3	1	Method NEAR3 Camouflaging NEAR3 Functions NEAR3 Electronic NEAR3 Circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2011/01/13 11:30
S4	8	kerveros AND fingerprint $3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2011/01/13 11:36
S5	1	kerveros AND fingerprint $3 AND test$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2011/01/13 11:38
S6	17	instantiation WITH circuit WITH compar$5	US-PGPUB; USPAT	OR	ON	2011/01/13 11:40
S9	8	"714"/$.ccls. and instantiation WITH compar $5	US-PGPUB; USPAT	OR	ON	2011/01/13 11:42
S8	301	instantiation WITH compar $5	US-PGPUB; USPAT	OR	ON	2011/01/13 11:42
S10	481	"714"/$.ccls. and tolerance WITH compar$5	US-PGPUB; USPAT	OR	ON	2011/01/13 11:44
S11	3	"714"/$.ccls. and manufacturing ADJ tolerance WITH compar$5	US-PGPUB; USPAT	OR	ON	2011/01/13 11:45
S13	46	"714"/$.ccls. and (manufacturing design) ADJ tolerance WITH circuit	US-PGPUB; USPAT	OR	ON	2011/01/13 11:46

S12	5	"714"/$.ccls. and (manufacturing design) ADJ tolerance WITH compar$5	US-PGPUB; USPAT	OR	ON	2011/01/13 11:46
S14	25	"714"/724-745.ccls. and (manufacturing design) ADJ tolerance WITH circuit	US-PGPUB; USPAT	OR	ON	2011/01/13 11:47
S15	24	"714"/724-745.ccls. and (manufacturing design) ADJ tolerance WITH circuit AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 11:48
S17	1	"714"/724-745.ccls. and (manufacturing design) ADJ tolerance WITH compar$5 AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 11:50
S18	43	"714"/724-745.ccls. and (manufacturing design) ADJ tolerance AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 11:54
S19	8	"714"/724-745.ccls. and (manufacturing design) with parameter with tolerance AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 11:56
S22	62	"714"/724-745.ccls. and vot$3 with (circuit gate) AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 11:58
S21	1	"714"/724-745.ccls. and vot$3 with parameter AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 11:58
S23	10	"714"/724-745.ccls. and vot$3 with (identical equivalent) NEAR3 circuit AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 12:22
S25	25	"714"/724-745.ccls. and (compar$5 vot$3) NEAR3 (identical equivalent) NEAR3 circuit AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 12:28
S24	101	"714"/724-745.ccls. and (compar$5 vot$3) with (identical equivalent) NEAR3 circuit AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 12:28

S26	183	("324"/$ "714"/$).ccls. and (compar$5 vot$3) NEAR3 (identical equivalent) NEAR3 circuit AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 12:34
S27	307	("324"/$ "714"/$).ccls. and (compar$5 vot$3) NEAR3 (identical equivalent) WITH circuit AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 12:36
S28	5	("324"/$ "714"/$).ccls. and (compar$5 vot$3) NEAR3 (identical equivalent) WITH circuit WITH identif$5 AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 12:37
S30	2	("324"/$ "714"/$).ccls. and (compar$5 vot$3) NEAR3 (identical equivalent) WITH circuit WITH tolerance AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 12:38
S31	8	"714"/724-745.ccls. and signature with (identical equivalent) NEAR3 circuit AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/01/13 12:47
S35	3	"714"/724-745.ccls. and (manufacturing factory) with parameter with stor $3 AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/04/13 14:07
S34	1	"714"/724-745.ccls. and (manufacturing factory) NEAR3 parameter WITH stor$3 AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/04/13 14:07
S36	35	"714"/724-745.ccls. and (manufactur$ factory) with (parameter value) with stor$3 AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/04/13 14:08
S37	16	"714"/724-745.ccls. and (manufactur$ factory) with (parameter value) with stor$3 WITH (chip circuit) AND @ad<"20070911"	US-PGPUB; USPAT	OR	ON	2011/04/13 14:09

| S39 | 4 | "714"/724-745.ccls. and (manufactur$ factory) NEAR5 (parameter value) with stor$3 WITH (chip circuit) AND @ad<"20070911" | US-PGPUB; USPAT | OR | | ON | 2011/04/13 14:10 |

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S41	11	(manufacturing design) with parameter with tolerance	UPAD	OR	ON	2011/04/13 14:04
S40	1	signature with (identical equivalent) NEAR3 circuit	UPAD	OR	ON	2011/04/13 14:04
S44	25	(manufacturing factory) with parameter with stor$3	UPAD	OR	ON	2011/04/13 14:05
S42	6	(manufacturing factory) with parameter with tolerance	UPAD	OR	ON	2011/04/13 14:05

4/13/11 2:13:24 PM
C:\ Documents and Settings\ jkerveros\ My Documents\ EAST\ Workspaces\ 10584592.wsp